L Number	Hite	Search Text	DB	Time stamp
2	1	6185428.pn. and processor	USPAT;	2003/09/29 12:40
_	_	orosizotpini diid processor	EPO; JPO	2003,03,23 12.10
3	9	(insert\$3 or add\$3) same delay\$3 and	USPAT;	2003/09/29 12:43
		(memory or buffer\$3) same (shift near4	EPO; JPO	
		register) same cyclical\$3 with address\$3		
5	56		USPAT;	2003/09/29 12:45
		(memory or buffer\$3) same (shift near4	EPO; JPO	
_	1	register) and cyclical\$3 with address\$3		
7	40	'',',',',',',',',',',',',',',',	USPAT;	2003/09/29 12:44
		(memory or buffer\$3) and (shift near4 register) and cyclical\$3 with address\$3	EPO; JPO	
		with memory		
9	40	1	USPAT;	2003/09/29 12:45
		(memory or buffer\$3) and (shift near4	EPO; JPO	2003,03,23 12.43
		register) and cyclical\$3 with address\$3		
		with memory) and @ad<20000920		
10	56	((insert\$3 or add\$3) same delay\$3 and	USPAT;	2003/09/29 12:45
		(memory or buffer\$3) same (shift near4	EPO; JPO	
		register) and cyclical\$3 with address\$3)		
	_	and @ad<20000920		
11	5	, , , , , , , , , , , , , , , , , , , ,	USPAT;	2003/09/29 12:46
		(memory or buffer\$3) same (shift near4	EPO; JPO	
		register) same cyclical\$3 with address\$3 with memory		
12	5	1	USPAT;	2003/09/29 12:47
	Ĭ	(memory or buffer\$3) same (shift near4	EPO; JPO	2003/03/23 12.47
		register) same cyclical\$3 with address\$3	220, 020	
	i	with memory) and @ad<20000920		
16	40	(insert\$3 or add\$3) same delay\$3 and	USPAT;	2003/09/29 12:49
		(memory or buffer\$3) and (shift near4	EPO; JPO	
		register) and cyclical\$3 with address\$3		+
1.7		with memory		
17	40	((insert\$3 or add\$3) same delay\$3 and	USPAT;	2003/09/29 12:52
		(memory or buffer\$3) and (shift near4	EPO; JPO	
		register) and cyclical\$3 with address\$3 with memory) and @ad<20000920		
18	32	(((insert\$3 or add\$3) same delay\$3 and	USPAT;	2003/09/29 12:49
		(memory or buffer\$3) and (shift near4	EPO; JPO	2003,03,23 12.13
		register) and cyclical\$3 with address\$3	,	
		with memory) and @ad<20000920) and ((base		
		near4 station) or MS)		
20	1	(insert\$3 or add\$3) same delay\$3 and (base	USPAT;	2003/09/29 12:50
		with station) and (shift near4 register)	EPO; JPO	
22	50	and cyclical\$3 with address\$3 with memory		0000/00/00 10 50
22	30	(base near4 station) same (memory or buffer\$3) same shift with register	USPAT;	2003/09/29 12:52
23	1	((base near4 station) same (memory or	EPO; JPO USPAT;	2003/09/29 12:52
23	_	buffer\$3) same shift with register) and	EPO; JPO	2003/09/29 12:52
		cyclical\$3 with address\$3 with memory	1210, 010	
24	613	(base near4 station) and (memory or	USPAT;	2003/09/29 13:00
		buffer\$3) same shift with register	EPO; JPO	
25	7	((base near4 station) and (memory or	USPAT;	2003/09/29 12:57
		buffer\$3) same shift with register) and	EPO; JPO	
0.5		cyclical\$3 with address\$3 with memory		
26	6	(((base near4 station) and (memory or	USPAT;	2003/09/29 12:59
		buffer\$3) same shift with register) and	EPO; JPO	
		cyclical\$3 with address\$3 with memory) and @ad<20000920		
27	6	((((base near4 station) and (memory or	USPAT;	2003/09/29 12:54
_ '		buffer\$3) same shift with register) and	EPO; JPO	2003/03/23 12:54
		cyclical\$3 with address\$3 with memory) and	110, 010	
		@ad<20000920) and delay\$3		
28	10	((base near4 station) and (memory or	USPAT;	2003/09/29 13:01
		buffer\$3) same shift with register) and	EPO; JPO	
		cyclical\$3 with address\$3		
29	9	(((base near4 station) and (memory or	USPAT;	2003/09/29 13:00
		buffer\$3) same shift with register) and	EPO; JPO	
		cyclical\$3 with address\$3) and		
31	56	@ad<20000920 (base near4 station) same (memory or	tionam.	2002/00/20 12 22
J	30	buffer\$3) same shift with register	USPAT;	2003/09/29 13:00
	l	Sarreral same suric with redister	EPO; JPO	<u> </u>

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32	54	((base near4 station) same (memory or	USPAT;	2003/09/29 13:00
		buffer\$3) same shift with register) and	EPO; JPO	
İ	}	@ad<20000920		
34	3	(((base near4 station) same (memory or	USPAT;	2003/09/29 13:03
		buffer\$3) same shift with register) and	EPO; JPO]
İ		@ad<20000920) and cyclical\$3		
35	2	((((base near4 station) same (memory or	USPAT;	2003/09/29 13:03
		buffer\$3) same shift with register) and	EPO; JPO	
		@ad<20000920) and cyclical\$3) and delay\$3	1	